

Electronic Acknowledgement Receipt

EFS ID:	2749451		
Application Number:	10083872		
International Application Number:			
Confirmation Number:	4721		
Title of Invention:	Multi-cluster processor for processing instructions of one or more instruction threads		
First Named Inventor/Applicant Name:	Eric DeLano		
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Filer:	Kyle J. Way/Jamie Cameron		
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Application Type:	Utility under 35 USC 111(a)		

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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	20080122RCE.pdf	28496 52080d0fb0f518b0c97d930d5921294997 6e595e	no	2
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2	Extension of Time	20080122ExtofTime.pdf	23456 5dab38325b09d3d53dc7034846b6d9b 40beuu14	no	1
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3	Amendment Submitted/Entered with Filing of CPA/RCE	20080122ResponsetoAA.pdf	57567 5ba0378ccac6b24c6ff13021643b2818b 4b99093	no	9
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